



Welcome United States Patent and Trademark Office

Search Results**BROWSE****SEARCH****IEEE XPLORE GUIDE**

Results for "(differential amplifier and feedback<in>metadata)"

Your search matched 22 of 1142142 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance** in **Descending** order.[» View Session History](#)[» New Search](#)[» Key](#)

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

Modify Search

(differential amplifier 'and' feedback<in>metadata)

☐ Check to search only within this results set**Display Format:** ☒ Citation ☐ Citation & Abstract**Select Article Information**

- | | |
|--------------------------|--|
| <input type="checkbox"/> | 1. A new high-dynamic range dual-loop power-to-current amplifier van Zeijl, P.T.M.; Solid-State Circuits, IEEE Journal of Volume 24, Issue 3, Jun 1989 Page(s):646 - 650 AbstractPlus Full Text: PDF (420 KB) IEEE JNL |
| <input type="checkbox"/> | 2. Fully differential operational amplifiers with accurate output balancing Banu, M.; Khoury, J.M.; Tsividis, Y.; Solid-State Circuits, IEEE Journal of Volume 23, Issue 6, Dec. 1988 Page(s):1410 - 1414 AbstractPlus Full Text: PDF (460 KB) IEEE JNL |
| <input type="checkbox"/> | 3. Virtual-ground sensing techniques for a 49-ns/200-MHz access time 1.8-V 256-Mb flash memory Le, B.Q.; Achter, M.; Chin Ghee Chng; Xin Guo; Cleveland, L.; Pau-Ling Chen; Van Bu R.W.; Solid-State Circuits, IEEE Journal of Volume 39, Issue 11, Nov. 2004 Page(s):2014 - 2023 AbstractPlus References Full Text: PDF (880 KB) IEEE JNL |
| <input type="checkbox"/> | 4. An 8-Gb/s capacitively coupled receiver with high common-mode rejection for ur Maillard, X.; Kuijk, M.; Solid-State Circuits, IEEE Journal of Volume 39, Issue 11, Nov. 2004 Page(s):1909 - 1915 AbstractPlus References Full Text: PDF (1160 KB) IEEE JNL |
| <input type="checkbox"/> | 5. Nonlinear effects in pseudo differential OTAs with CMFB Mohieldin, A.N.; Sanchez-Sinencio, E.; Silva-Martinez, J.; Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [s and Systems II: Express Briefs, IEEE Transactions on] Volume 50, Issue 10, Oct. 2003 Page(s):762 - 770 AbstractPlus References Full Text: PDF (654 KB) IEEE JNL |
| <input type="checkbox"/> | 6. A fully balanced pseudo-differential OTA with common-mode feedforward and in mode feedback detector Mohieldin, A.N.; Sanchez-Sinencio, E.; Silva-Martinez, J.; Solid-State Circuits, IEEE Journal of |

Volume 38, Issue 4, April 2003 Page(s):663 - 668

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(485 KB\)](#) IEEE JNL

- ☐ 7. **A 1-V transformer-feedback low-noise amplifier for 5-GHz wireless LAN in 0.18- μ m CMOS**
Cassan, D.J.; Long, J.R.;
Solid-State Circuits, IEEE Journal of
Volume 38, Issue 3, March 2003 Page(s):427 - 435
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(751 KB\)](#) IEEE JNL
- ☐ 8. **High linearity and high efficiency of class-B power amplifiers in GaN HEMT technology**
Paidi, V.; Shouxuan Xie; Coffie, R.; Moran, B.; Heikman, S.; Keller, S.; Chini, A.; DenB; Mishra, U.K.; Long, S.; Rodwell, M.J.W.;
Microwave Theory and Techniques, IEEE Transactions on
Volume 51, Issue 2, Feb. 2003 Page(s):643 - 652
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(723 KB\)](#) IEEE JNL
- ☐ 9. **A bootstrap technique for wideband amplifiers**
Centurelli, F.; Luzzi, R.; Olivieri, M.; Trifiletti, A.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [s
and Systems I: Regular Papers, IEEE Transactions on]
Volume 49, Issue 10, Oct. 2002 Page(s):1474 - 1480
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(342 KB\)](#) IEEE JNL
- ☐ 10. **Adaptive analog IF signal processor for a wide-band CMOS wireless receiver**
Behbahani, F.; Karimi-Sanjaani, A.; Wee-Guan Tan; Roithmeier, A.; Leete, J.C.; Hoshii
Solid-State Circuits, IEEE Journal of
Volume 36, Issue 8, Aug. 2001 Page(s):1205 - 1217
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(300 KB\)](#) IEEE JNL
- ☐ 11. **A systematic approach in constructing fully differential amplifiers**
Gonggui Xu; Embabi, S.H.K.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [s
and Systems II: Express Briefs, IEEE Transactions on]
Volume 47, Issue 11, Nov 2000 Page(s):1343 - 1347
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(128 KB\)](#) IEEE JNL
- ☐ 12. **A systematic approach in constructing fully differential amplifiers**
Gonggui Xu; Embabi, S.H.K.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [s
and Systems II: Express Briefs, IEEE Transactions on]
Volume 47, Issue 12, Dec. 2000 Page(s):1547 - 1550
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(116 KB\)](#) IEEE JNL
- ☐ 13. **A CMOS optical preamplifier for wireless infrared communications**
Khoman Phang; Johns, D.A.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [s
and Systems II: Express Briefs, IEEE Transactions on]
Volume 46, Issue 7, July 1999 Page(s):852 - 859
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(308 KB\)](#) IEEE JNL
- ☐ 14. **Analysis and compensation of two-pole amplifiers with a pole-zero doublet**
Palmisano, G.; Palumbo, G.;
Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on [s
and Systems I: Regular Papers, IEEE Transactions on]
Volume 46, Issue 7, July 1999 Page(s):864 - 868
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(192 KB\)](#) IEEE JNL

- ☐ **15. Current-mode approach for wide-gain bandwidth product architecture**
Lee, C.-H.; Cornish, J.; McClellan, K.; Choma, J., Jr.;
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [s
and Systems II: Express Briefs, IEEE Transactions on]
Volume 45, Issue 5, May 1998 Page(s):626 - 631
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(220 KB\)](#) IEEE JNL

- ☐ **16. Common-mode rejection ratio redefined**
Koide, F.T.;
Instrumentation and Measurement, IEEE Transactions on
Volume 46, Issue 3, June 1997 Page(s):737 - 739
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(112 KB\)](#) IEEE JNL

- ☐ **17. A 12 GHz 30 dB modular BICMOS limiting amplifier for 10 Gb SONET receiver**
Kim, H.; Bauman, J.;
Solid-State Circuits Conference, 2000. Digest of Technical Papers. ISSCC. 2000 IEEE
7-9 Feb. 2000 Page(s):160 - 161, 453
[AbstractPlus](#) | Full Text: [PDF\(275 KB\)](#) IEEE CNF

- ☐ **18. Low voltage, high speed fully differential CMOS op amp**
Grech, I.; Micallef, J.; Vladimirova, T.;
Electronics, Circuits and Systems, 2000. ICECS 2000. The 7th IEEE International Con
Volume 1, 17-20 Dec. 2000 Page(s):7 - 10 vol.1
[AbstractPlus](#) | Full Text: [PDF\(288 KB\)](#) IEEE CNF

- ☐ **19. A fully balanced programmable sample-hold amplifier for low-voltage applicator**
Frag, F.A.; Schneide, M.C.; Galup-Montoro, C.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE Intern
Symposium on
Volume 3, 28-31 May 2000 Page(s):443 - 446 vol.3
[AbstractPlus](#) | Full Text: [PDF\(212 KB\)](#) IEEE CNF

- ☐ **20. The new CMOS 2 V low-power IF fully differential Rm-C bandpass amplifier for RI
receivers**
Chung-Yu Wu; Yu Cheng; Jeng Gong;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE Intern
Symposium on
Volume 2, 28-31 May 2000 Page(s):633 - 636 vol.2
[AbstractPlus](#) | Full Text: [PDF\(248 KB\)](#) IEEE CNF

- ☐ **21. An accurate self-bias threshold voltage extractor using differential difference fee**
Cilingiroglu, U.; Hoon, S.K.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE Intern
Symposium on
Volume 5, 28-31 May 2000 Page(s):209 - 212 vol.5
[AbstractPlus](#) | Full Text: [PDF\(252 KB\)](#) IEEE CNF

- ☐ **22. New multiple input fully differential variable gain CMOS instrumentation amplifie**
Gano, A.J.; Franca, J.E.;
Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE Intern
Symposium on
Volume 4, 28-31 May 2000 Page(s):449 - 452 vol.4
[AbstractPlus](#) | Full Text: [PDF\(316 KB\)](#) IEEE CNF

[View Selected Items](#)



[Help](#) [Contact Us](#) [Privacy & :](#)

© Copyright 2005 IEEE –

☐ Display
 ☒ If graph is not visible, display

Initial graph:

1 and meeting of output and nonmeeting of output

Read: 34/34